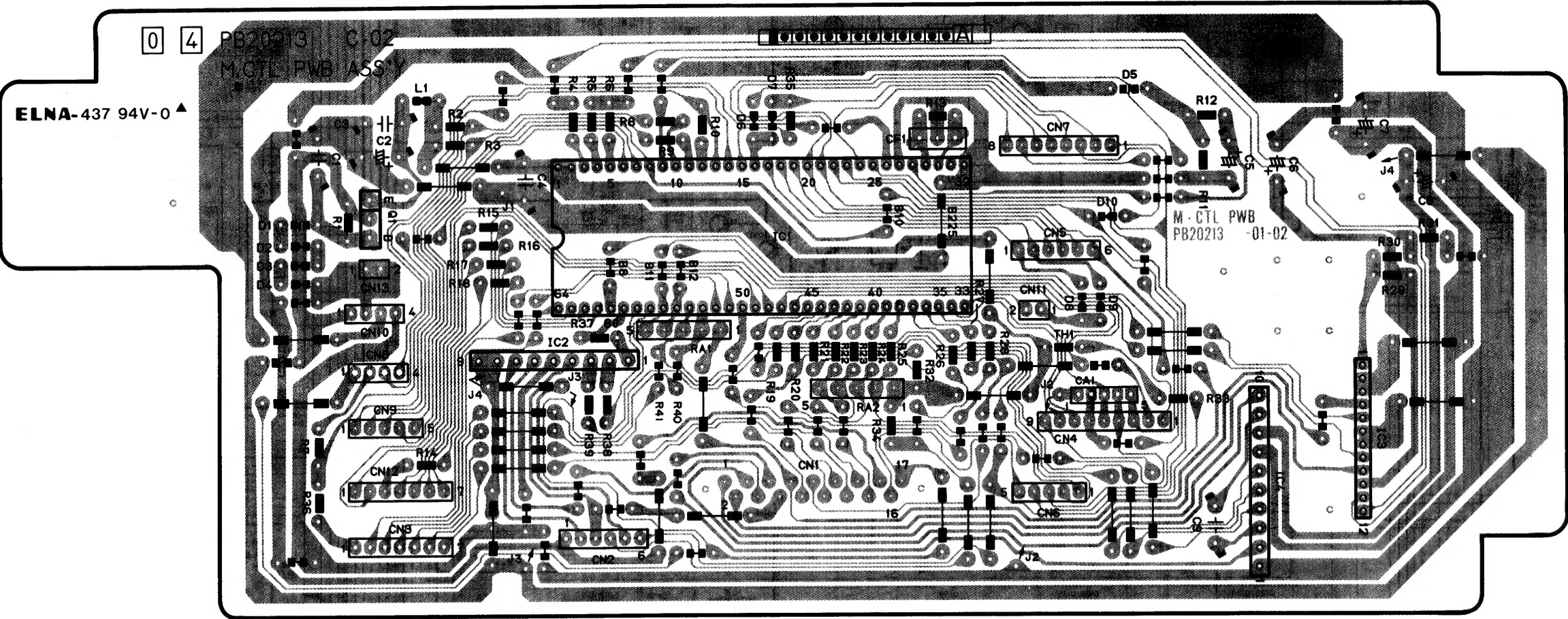
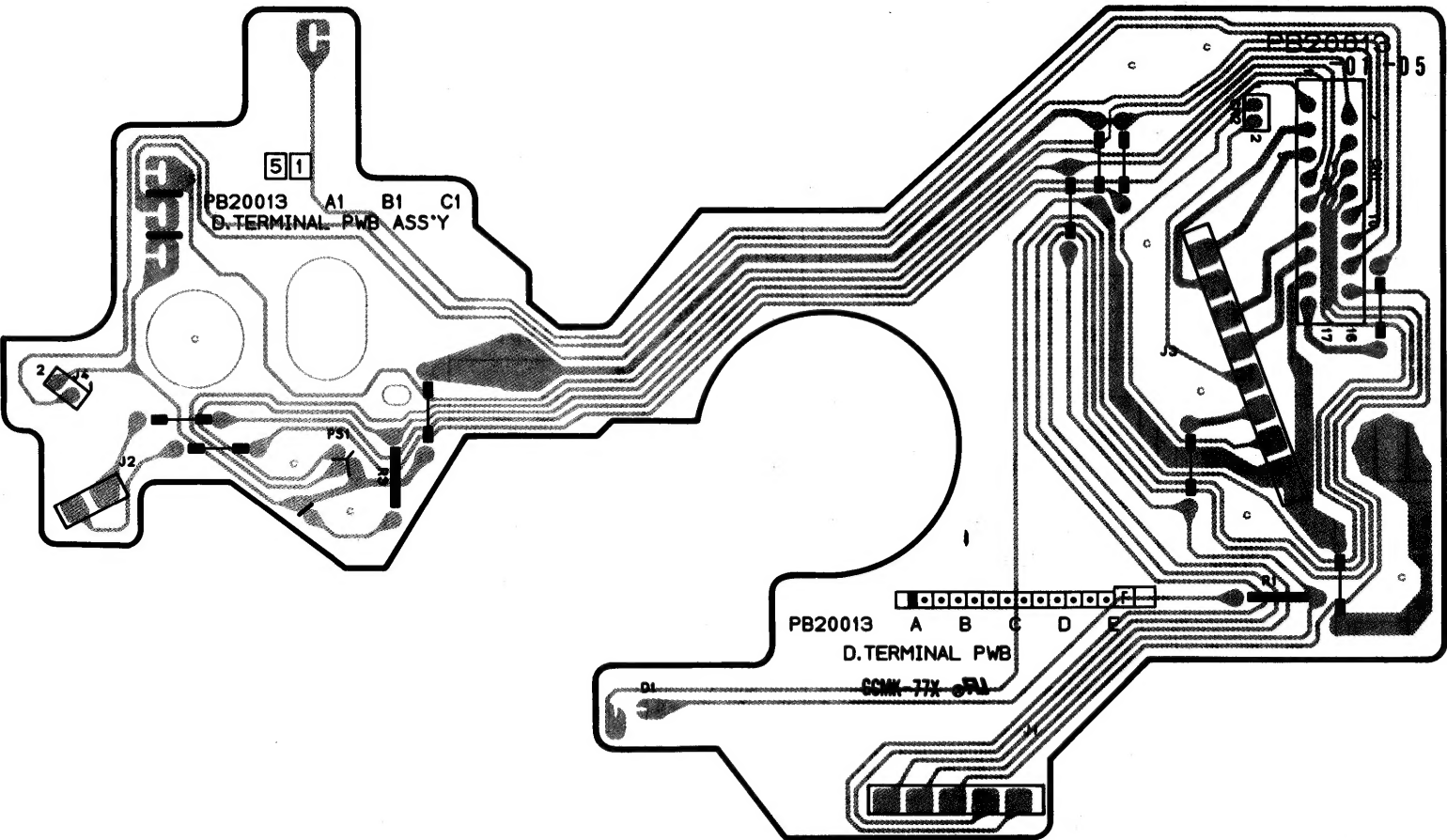


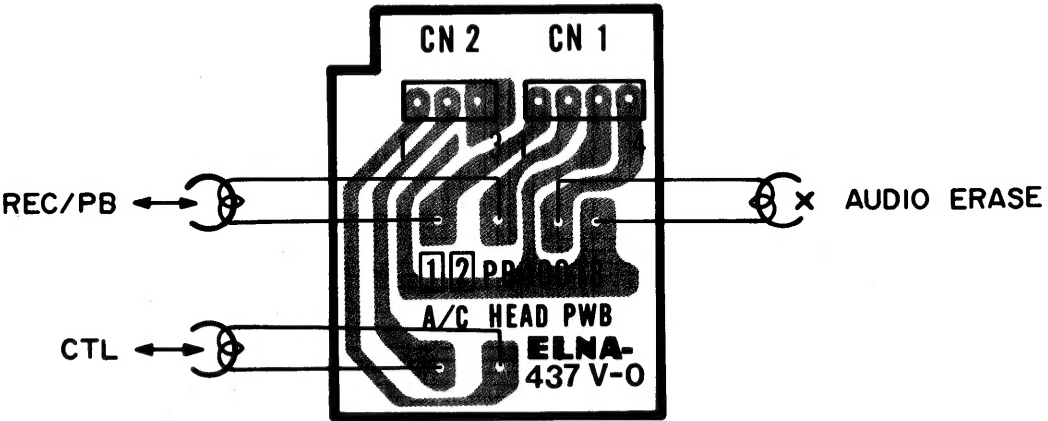
— MECHANISM CONTROL —



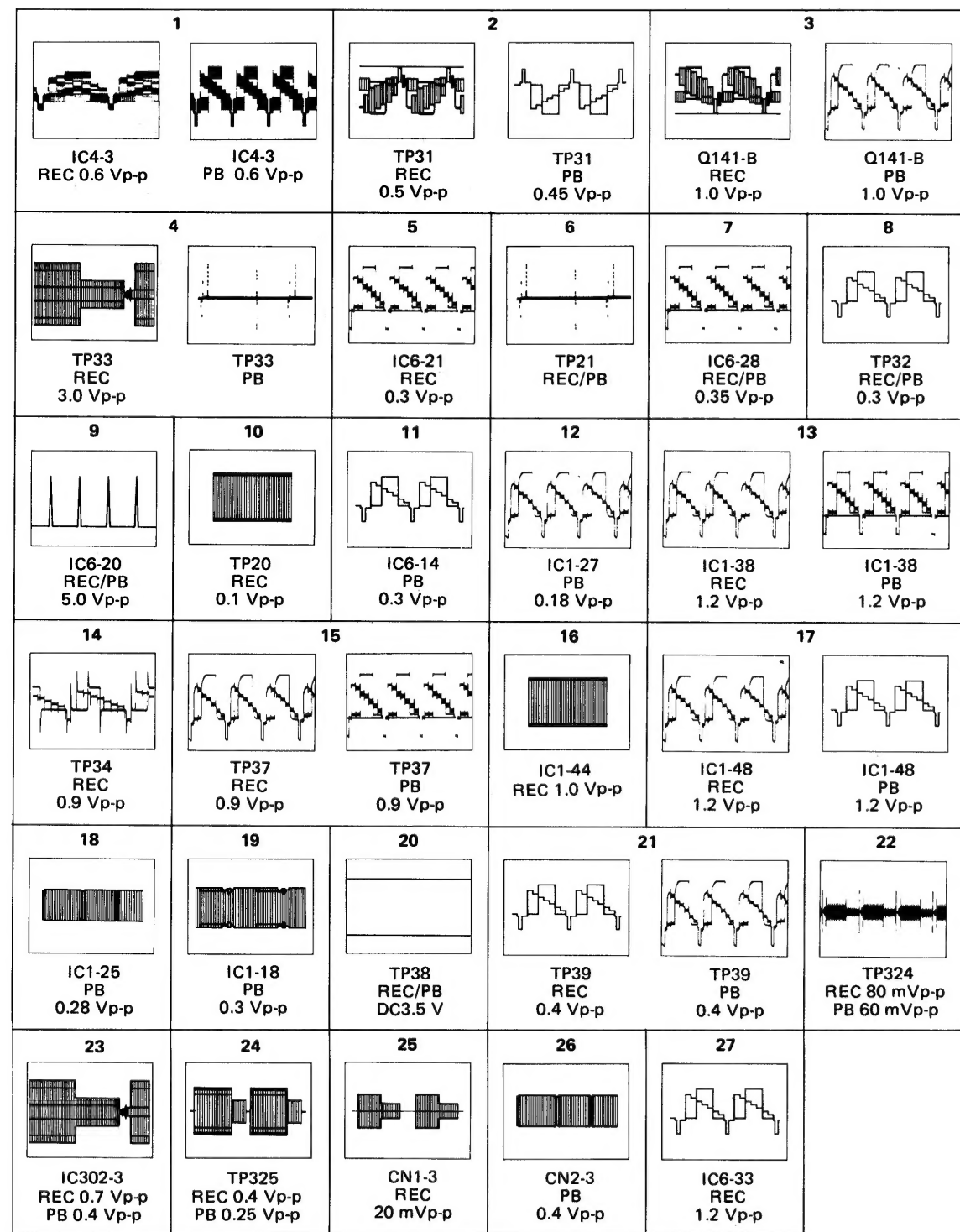
— DECK TERMINAL —



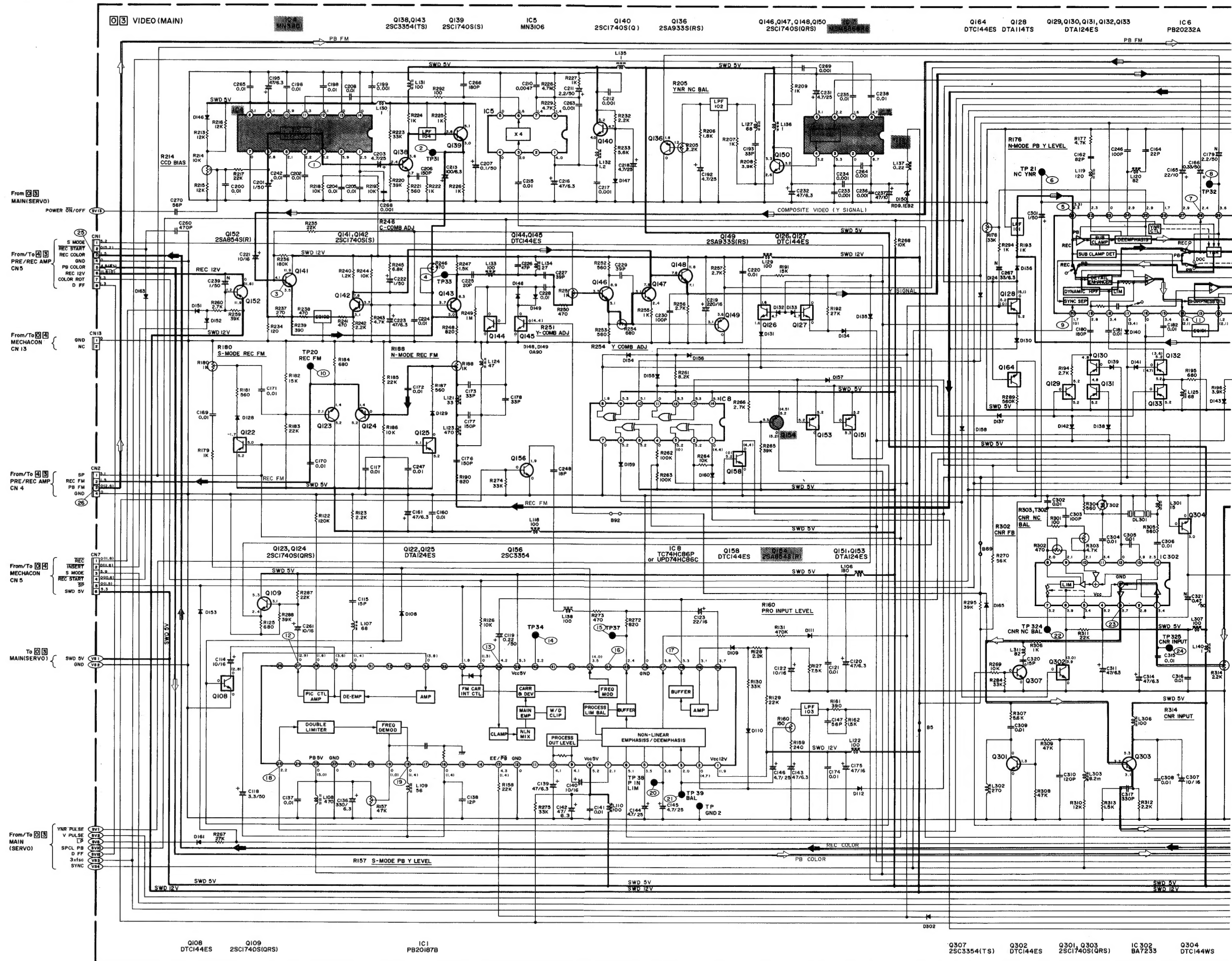
— A/C HEAD —



### 3.16 VIDEO (VHS MODE) SCHEMATIC DIAGRAMS



Note: Signal flow lines (REC/PB) are shown in normal VHS mode which supplied by external input signal (VIDEO IN).

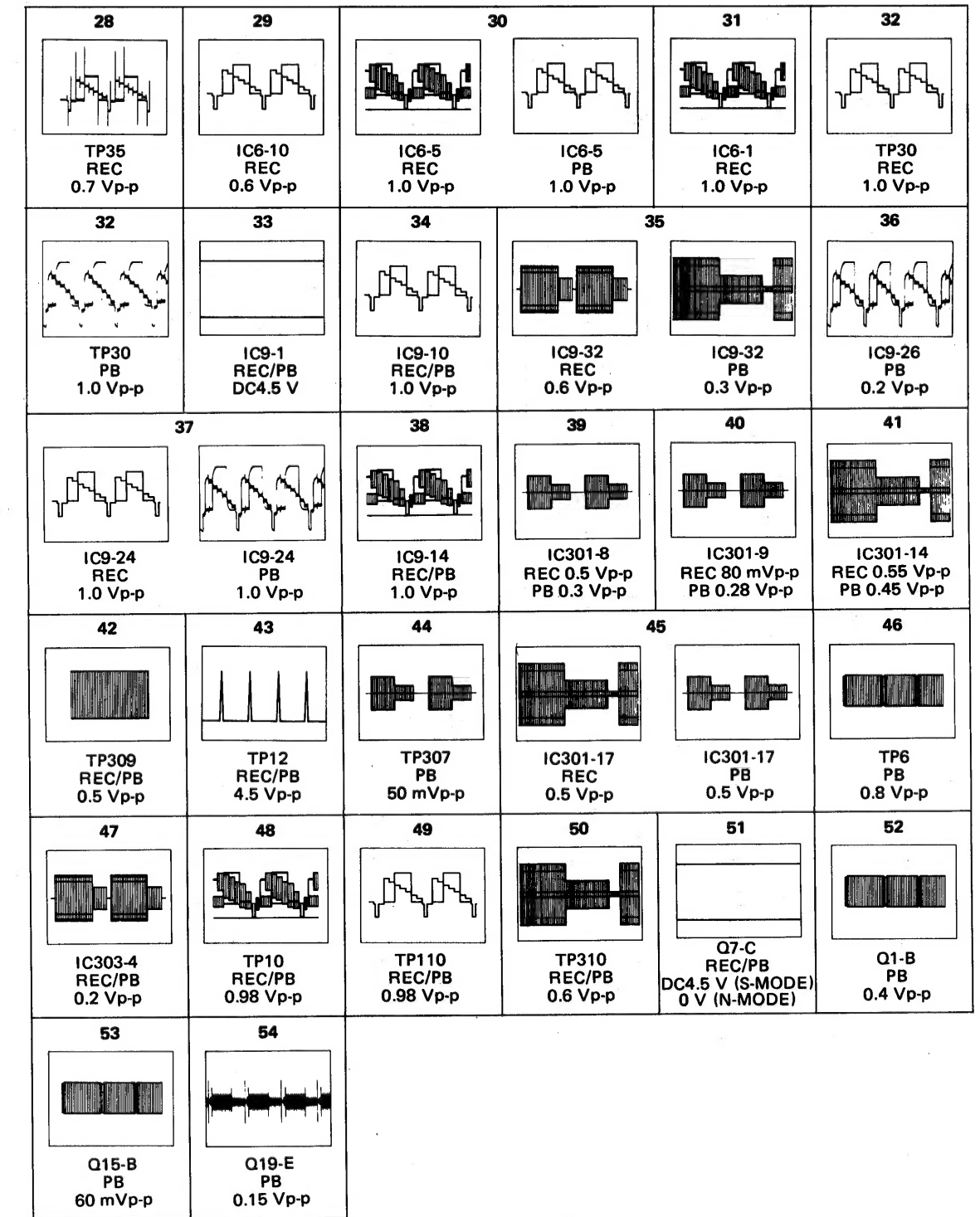




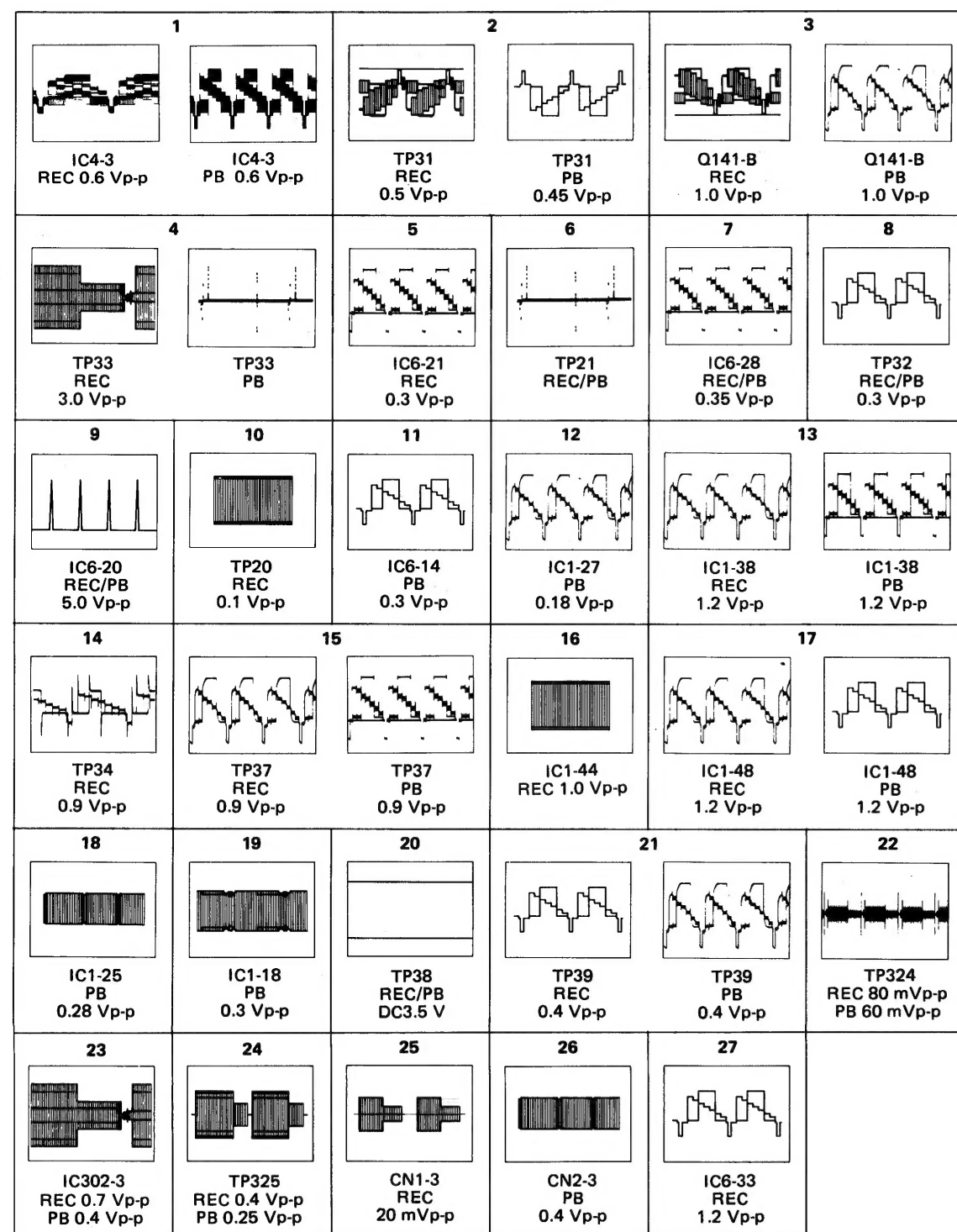
The diagram is a comprehensive electronic schematic for a television receiver. It features a variety of components including:

- Transistors:** Q1 through Q19, including 2SC1740S(QRS), 2SA933S(RS), and DTA124ES.
- Integrated Circuits:** IC1 through IC11, including 2SC1740S(QRS), 2SA933S(RS), and DTA124ES.
- Resistors:** R1 through R40, with values ranging from 10K to 100K.
- Capacitors:** C1 through C40, with values ranging from 0.01 to 1000.
- Connectors:** CN1 through CN6, including a terminal block for video input/output.
- Other Components:** TP1 through TP4, SW1 through SW4, and various passive components like diodes and inductors.

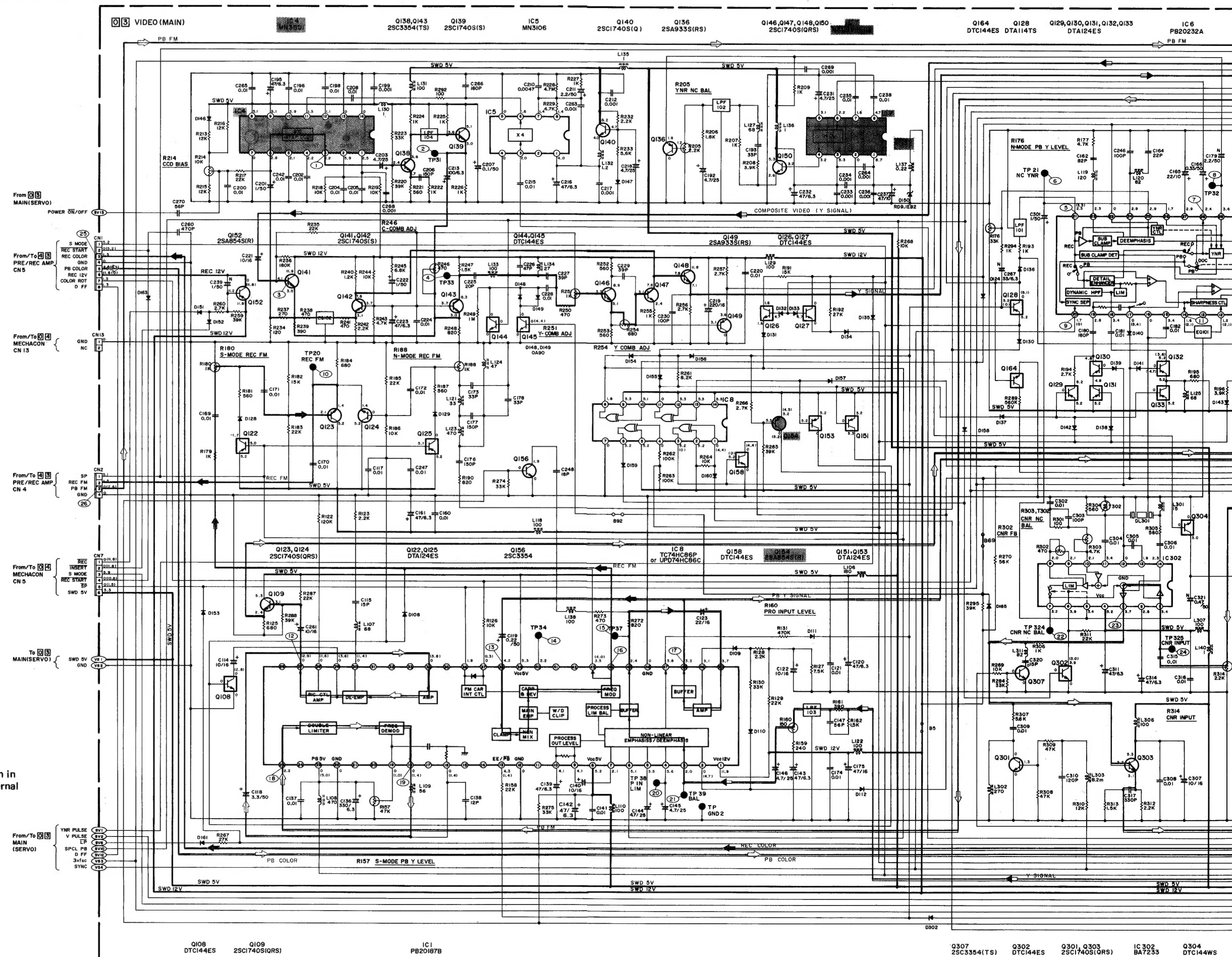
The diagram is labeled with component values and pin numbers. It includes a terminal block for video input/output and a remote control interface. The schematic is organized into several functional blocks, including a video input section, a video processing section, a video output section, and a remote control interface.



### 3.17 VIDEO (S-VHS MODE) SCHEMATIC DIAGRAM



Note: Signal flow lines (REC/PB) are shown in S-VHS mode which supplied by external input signal (VIDEO IN).



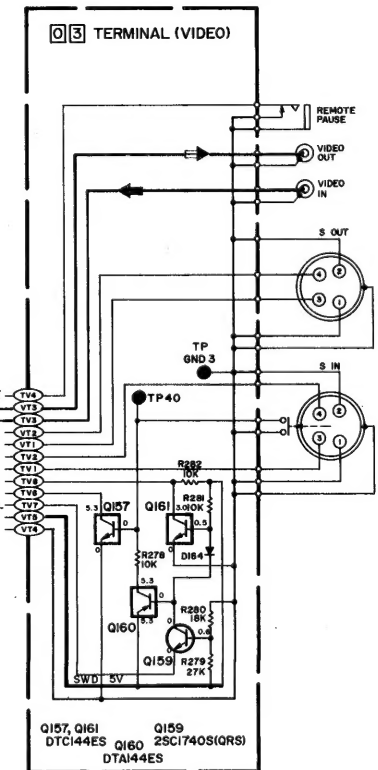


The schematic diagram illustrates the internal circuitry of a television receiver, organized into several functional blocks:

- Power Supply Section (Top Right):** Features a transformer (T1) with multiple secondary windings for SWD 5V, SWD 12V, and SWD 12V. It includes a full-wave rectifier bridge (D1-D4) and a filter capacitor (C1). A 5V regulator (Q1) and a 12V regulator (Q2) are shown, along with a 12V regulator (Q3) and a 12V regulator (Q4).
- Tuner Section (Middle):** Contains a tuner IC (IC1) and a tuner IC (IC2). It includes a tuner IC (IC3) and a tuner IC (IC4). The tuner section is connected to a video input terminal (Q1) and a video output terminal (Q2).
- Video Section (Bottom Right):** Includes a video IC (IC1) and a video IC (IC2). It features a video IC (IC3) and a video IC (IC4). The video section is connected to a video input terminal (Q1) and a video output terminal (Q2).
- Other Components:** The diagram includes numerous resistors (R1-R40) and capacitors (C1-C40) distributed throughout the circuit. It also shows a terminal block (Q1) for video input and output, and a power supply section (Q1-Q4) for SWD 5V, SWD 12V, and SWD 12V.

The schematic is a detailed representation of the television receiver's internal circuitry, showing the interconnection of various electronic components and their functional blocks.

<p>28</p> <p>TP35 REC 0.7 Vp-p</p>	<p>29</p> <p>IC6-10 REC 0.6 Vp-p</p>	<p>30</p> <p>IC6-5 REC 1.0 Vp-p</p>	<p>31</p> <p>IC6-1 REC 1.0 Vp-p</p>	<p>32</p> <p>TP30 REC 1.0 Vp-p</p>
<p>32</p> <p>TP30 PB 1.0 Vp-p</p>	<p>33</p> <p>IC9-1 REC/PB DC4.5 V</p>	<p>34</p> <p>IC9-10 REC/PB 1.0 Vp-p</p>	<p>35</p> <p>IC9-32 REC 0.6 Vp-p</p>	<p>36</p> <p>IC9-26 PB 0.2 Vp-p</p>
<p>37</p> <p>IC9-24 REC 1.0 Vp-p</p>	<p>38</p> <p>IC9-24 PB 1.0 Vp-p</p>	<p>39</p> <p>IC301-8 REC 0.5 Vp-p PB 0.3 Vp-p</p>	<p>40</p> <p>IC301-9 REC 80 mVp-p PB 0.28 Vp-p</p>	<p>41</p> <p>IC301-14 REC 0.55 Vp-p PB 0.45 Vp-p</p>
<p>42</p> <p>TP309 REC/PB 0.5 Vp-p</p>	<p>43</p> <p>TP12 REC/PB 4.5 Vp-p</p>	<p>44</p> <p>TP307 PB 50 mVp-p</p>	<p>45</p> <p>IC301-17 REC 0.5 Vp-p</p>	<p>46</p> <p>TP6 PB 0.8 Vp-p</p>
<p>47</p> <p>IC303-4 REC/PB 0.2 Vp-p</p>	<p>48</p> <p>TP10 REC/PB 0.98 Vp-p</p>	<p>49</p> <p>TP110 REC/PB 0.98 Vp-p</p>	<p>50</p> <p>TP310 REC/PB 0.6 Vp-p</p>	<p>51</p> <p>Q7-C REC/PB DC4.5 V (S-MODE) 0 V (N-MODE)</p>
<p>53</p> <p>Q15-B PB 60 mVp-p</p>	<p>54</p> <p>Q19-E PB 0.15 Vp-p</p>			





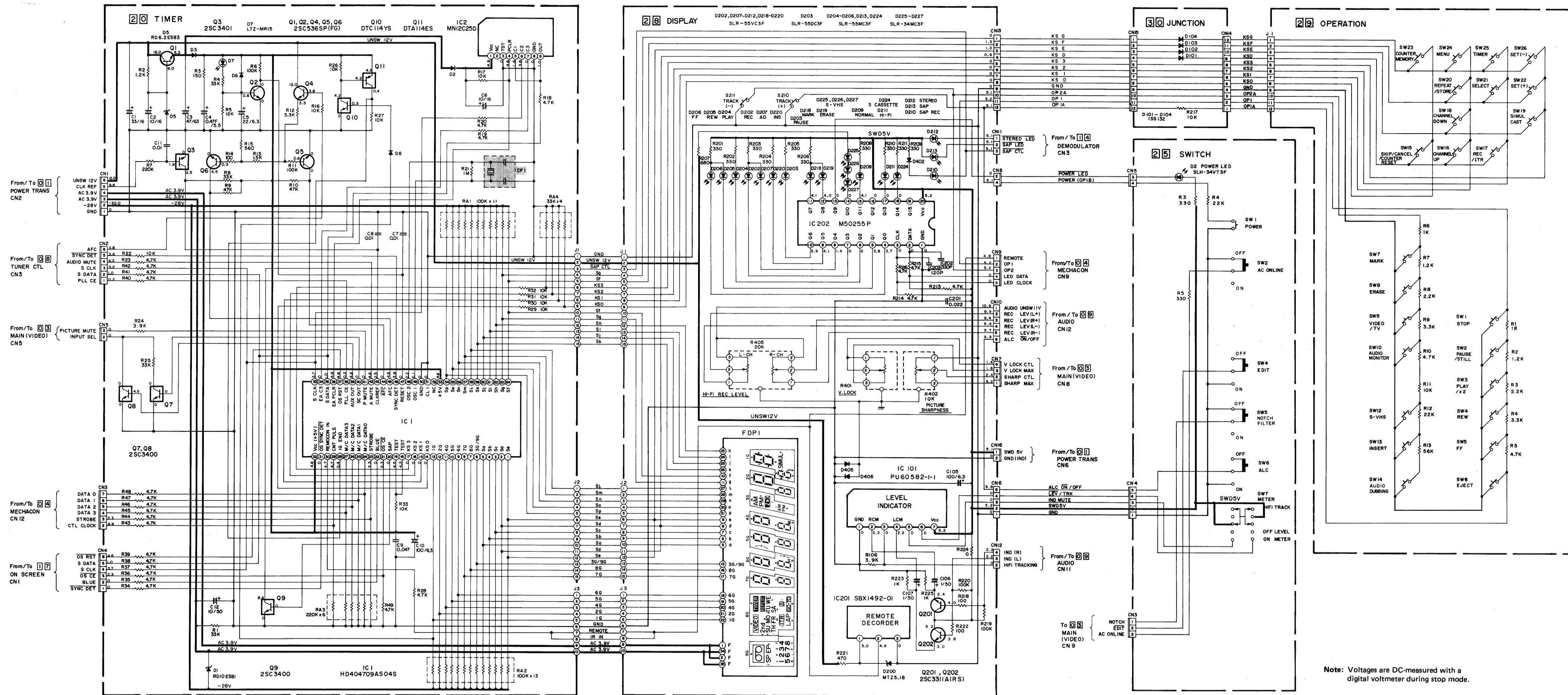












Note: Voltages are DC-measured with a digital voltmeter during stop mode.